



**SRI VENKATESWARA COLLEGE (University of Delhi)**  
**Benito Juarez Road, Dhaula Kuan, New Delhi- 110021**  
**TIME TABLE – 2025-26 (ODD Semester)**

**Department: Electronics**

**Semester: I (Hons)**

	1 8.45- 9.45 am	2 9.45-10.45 am	3 10.45-11.45 am	4 11.45-12.45 am/pm		5 01.05-2.05 pm	6 2.05-3.05 pm	7 3.05-4.05 pm	8 4.05-5.05 pm	
<b>MONDAY</b>		DSC 1 331 JL	DSC 1 LAB ICT Faculty LAB JL +RH		<b>12.45 – 1.05 pm</b>  <b>LUNCH BREAK</b>	GE I Th				
<b>TUESDAY</b>		DSC 1 258 JL	AEC EVS 159	AEC EVS 159		DSC 2 BI, DSC 3 BII Lab Electronics Lab I & II , 261 NK +HS	VAC Semester I HS			
<b>WEDNESDAY</b>	GE I, Th 257 RN	DSC 3 258 NK	DSC 2 BII, DSC 3 BI Lab Electronics Lab I & II, 261 NK + HS				DSC 2 257 HS	DSC 2 257 HS	*	
<b>THURSDAY</b>		DSC 2 251 HS	DSC 1 251 JL			AEC EVS 159			*	
<b>FRIDAY</b>			DSC 3 257 NK	DSC 3 257 NK		GE I, Th ICT Faculty Lab RN	GE I, Th ICT Faculty Lab RN	GE I Practical (ICT Faculty Lab)		
<b>SATURDAY</b>	SEC Semester I (ICT Lab) RN + HY (1-2)						VAC Semester I (ICT Lab) HS		AEC Sem I	

<b>Theory</b>	<b>DSC 1: Programming Fundamentals using Python</b>	<b>Prof. J Lalita (JL)</b>
	<b>DSC 2: Circuit Theory &amp; Network Analysis</b>	<b>Dr Hari Singh (HS)</b>
	<b>DSC 3: Semiconductor Devices</b>	<b>Dr Neeru Kumar (NK)</b>
	<b>GE I:</b>	<b>Dr Rakhi Narang (RN)</b>
<b>Practical</b>	<b>DSC 1: Programming Fundamentals using Python</b>	<b>Prof. J Lalita (JL) + Dr Rahul (RH)</b>
	<b>DSC 2: Circuit Theory &amp; Network Analysis</b>	<b>Dr Hari Singh (HS)</b>
	<b>DSC 3: Semiconductor Devices</b>	<b>Dr Neeru Kumar (NK)</b>
	<b>GE I</b>	<b>Dr Rakhi Narang (RN) + Dr Rahul (RH)</b>
	<b>SEC I</b>	<b>Dr Rakhi Narang (RN) + Dr Hina Yadav (HY)</b>
	<b>VAC I</b>	<b>Dr Hari Singh (HS)</b>


**Teacher in charge**



**SRI VENKATESWARA COLLEGE (University of Delhi)**  
**Benito Juarez Road, Dhaula Kuan, New Delhi- 110021**  
**TIME TABLE – 2025-26 (ODD Semester)**

**Department: Electronics**

**Semester: III (Hons)**

	1 8.45- 9.45 am	2 9.45-10.45 am	3 10.45-11.45 am	4 11.45-12.45 am/pm		5 01.05-2.05 pm	6 2.05-3.05 pm	7 3.05-4.05 pm	8 4.05-5.05 pm
<b>MONDAY</b>	GE III Th/ DSE III Th 257		DSC 8 BI Lab Electronics Lab I, 261 NJ		<b>12.45 – 1.05 pm LUNCH BREAK</b>	DSC 9 Lab ICT Faculty Lab RH +SJ			
<b>TUESDAY</b>	VAC Semester III RH		DSC 9 251 RH	*		AEC EVS 255	AEC EVS 255		
<b>WEDNESDAY</b>			DSC 7 257 TR	DSC 7 257 TR		GE III Th/ DSE III Th 257, RH	DSC 8 BII Lab Electronics Lab I, 261 NJ		*
<b>THURSDAY</b>	DSC 8 257 NJ		AEC EVS 159	DSC 7 B1 TR		GE III Th/ DSE III Th ICT FL, RH	GE III/DSE III Practical ICT Faculty Lab RH +SJ		*
<b>FRIDAY</b>	DSC 7 Lab ICT Faculty Lab TR + NJ		DSC 8 335 NJ	DSC 8 335 NJ		DSC 9 257 RH	DSC 9 257 RH		
<b>SATURDAY</b>	AEC Sem III		VAC Semester III ICT Lab HY			SEC Semester III  ICT Lab, TR + HS (7)			

<b>Theory</b>	<b>Papers</b>	<b>Name of the teacher</b>
	<b>DSC-7: Engineering Mathematics</b>	<b>Dr Tarakeswara Rao Kaviti (TR)</b>
	<b>DSC-8 Analog Electronics II</b>	<b>Dr Nutan Joshi (NJ)</b>
	<b>DSC-9 Signal and Systems</b>	<b>Dr Rahul (RH)</b>
	<b>GE-III /DSE-Sem III</b>	<b>Dr Rahul (RH)</b>
<b>Practical</b>	<b>DSC-7: Engineering Mathematics</b>	<b>Dr Tarakeswara Rao Kaviti (TR)</b>
	<b>DSC-8 Analog Electronics II</b>	<b>Dr Nutan Joshi (NJ)</b>
	<b>DSC-9 Signal and Systems</b>	<b>Dr Rahul (RH)</b>
	<b>GE-III /DSE-Sem III:</b>	<b>Dr Rahul (RH)</b>
	<b>SEC-III:</b>	<b>Dr Tarakeswara Rao Kaviti (TR) + Dr Hari Singh</b>
	<b>VAC-III:</b>	<b>Dr Hina Yadav (HY) + Dr Rahul (RH)</b>

**Teacher in charge**



<b>Theory</b>	<b>Papers</b>	<b>Name of the teacher</b>
	<b>DSC 13: Embedded Systems</b>	<b>Dr Sunita Jain (SJ)</b>
	<b>DSC 14: Electromagnetics</b>	<b>Dr Nutan Joshi (NJ)</b>
	<b>DSC 15: Basic VLSI Design</b>	<b>Dr Rakhi Narang (RN)</b>
	<b>DSE III: Computer Networks</b>	<b>Dr Hina Yadav (HY)</b>
	<b>GE V:</b>	<b>Dr Sunita Jain (SJ)</b>
<b>Practical</b>	<b>DSC 13: Embedded Systems</b>	<b>Dr Sunita Jain (SJ) + Dr Tarakeswara Rao Kaviti (TR)</b>
	<b>DSC 14: Electromagnetics</b>	<b>Dr Nutan Joshi (NJ) + Dr Hina Yadav (HY)</b>
	<b>DSC 15: Basic VLSI Design</b>	<b>Dr Rakhi Narang (RN) + Dr Neeru Kumar (NK)</b>
	<b>DSE-III: Computer Networks</b>	<b>Dr Hina Yadav (HY) + Dr Rakhi Narang (RN)</b>
	<b>GE V:</b>	<b>Dr Sunita Jain (SJ)</b>
	<b>SEC V:</b>	<b>Prof J Lalita (JL)</b>

**Teacher in charge**



**SRI VENKATESWARA COLLEGE (University of Delhi)**  
**Benito Juarez Road, Dhaula Kuan, New Delhi- 110021**  
**TIME TABLE – 2025-26 (ODD Semester)**

**Department: Electronics**

**Semester: VII (Hons)**

	1 8.45- 9.45 am	2 9.45-10.45 am	3 10.45-11.45 am	4 11.45-12.45 am/pm		5 01.05-2.05 pm	6 2.05-3.05 pm	7 3.05-4.05 pm	8 4.05-5.05 pm
<b>MONDAY</b>		DSC 257 HY		DSE 1 Th 336 NK	<b>12.45 – 1.05 pm LUNCH BREAK</b>	DSE 1 Th 336 NK	DSE 1 Practical Electronics Lab I, 261 +ICT faculty Lab NK		
<b>TUESDAY</b>	DSC 336 HY		DSE 2 Th 336 HS	*		GE VII Practical (ICT faculty Lab) TR		GE VII Th 336 TR	GE VII Th 336 TR
<b>WEDNESDAY</b>	DSE 3 Practical (ICT Faculty Lab)		DSE 3 Th 336 JL	DSE 3 Th 336 JL		Dissertation/Academic Project/Entrepreneurship *			
<b>THURSDAY</b>	DSC Practical (ICT Faculty Lab)		DSC 336 HY	DSE 2 Th 251 HS		DSE 3 Th 336 JL	DSE 1 Th 336 NK		*
<b>FRIDAY</b>			DSE 2 Th 336 HS	DSE 2 Th 336 HS		GE VII Th 336 TR			
<b>SATURDAY</b>	Dissertation/Academic Project/Entrepreneurship					Dissertation/Academic Project/Entrepreneurship			

<b>Theory</b>	<b>Papers</b>	<b>Name of the teacher</b>
	<b>DSC 19: Control Systems</b>	<b>Dr. Hina Yadav (HY)</b>
	<b>DSE 1: Digital Communication</b>	<b>Dr. Neeru Kumar (NK)</b>
	<b>DSE 2: Introduction to Nanoscience</b>	<b>Dr. Hari Singh (HS)</b>
	<b>DSE 3: Neural Networks and Deep Learning</b>	<b>Prof. J Lalita (JL)</b>
	<b>GE VII:</b>	<b>Dr. Tarakeswara Rao Kaviti (TR)</b>
<b>Practical</b>	<b>DSC 19: Control Systems</b>	<b>Dr. Hina Yadav (HY)</b>
	<b>DSE 1: Digital Communication</b>	<b>Dr. Neeru Kumar (NK)</b>
	<b>DSE 2: Introduction to Nanoscience</b>	<b>Dr. Hari Singh (HS)</b>
	<b>DSE 3: Neural Networks and Deep Learning</b>	<b>Prof. J Lalita (JL)</b>
		<b>GE VII:</b>

**Teacher in charge**